



Low Distortion Oscillator Series 3

Leaflet BC51 JUNE 75.

Technical Instructions Installation and Advanced Maintenance

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2. CONTROLS, FUNCTIONS AND FACILITIES

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1. GENERAL

The instrument leaves the factory set for operation on a 240 V supply. Do not switch on until the voltage selector on the rear chassis panel has been adjusted to the correct operating mains voltage.

The sales leaflet A51 Jan. 75 describes the general principles of design of the instrument, facilities provided, and specification details.

2. CONTROLS, FUNCTIONS AND FACILITIES

2.1 Output source resistance

The standard model LDO3 is supplied with either 600 ohm or 150 ohm attenuators, as required.

2.2 Floating output

The LDO3B includes an output amplifier driving a transformer producing a floating isolated output terminated by a 600 ohm step attenuator. The 0–20 dB attenuator is interposed between the oscillator amplifier and the output amplifier. The output source resistance of the attenuator therefore remains constant at 600 ohms regardless of the setting of the 0–20 dB attenuator. A control is provided on the front panel which switches the output from floating to unbalanced. The schematic diagram illustrates the circuit details applicable to both instruments.

2.3 'Oscillator off' facility

In the middle audio band the distortion of the standard model LDO is lower than the noise of conventional distortion measuring equipment. The 'oscillator off' switch inhibits oscillation in the instrument leaving the circuit conditions unchanged. If the rejected output from the distortion measuring equipment is displayed on an oscilloscope the contribution of hum and noise in the measuring set up can thus be determined.

3. CALIBRATION

3.1 Frequency adjustment

The frequency dial is set on the spindle of the potentiometer to read the correct frequency on the 1–10 kHz range at 3 kHz. The calibration accuracy at this setting of the dial on the other bands at 30 Hz, 300 Hz and 30 kHz is within 5%, typically 2% to 3%. Similarly the inaccuracies at the extremes of the dial marked 10 and 100 are also within 5% and typically 2% to 3% except at high frequencies between 60 and 100 kHz.

3.2 Output monitoring meter

The meter is a simple rectifier type and responds to the average value of the waveform calibrated in r.m.s. reading.

Sine wave adjustment P4 On sine wave output P4 is adjusted to read correct f.s.d. at 1.0 volt input against a reference voltmeter.

Square wave adjustment P5 P5 is adjusted for r.m.s. reading with a dynamometer or other true r.m.s. responding meter with a square wave input. If an average responding meter, calibrated for r.m.s. reading on a sine wave is used it should read 1.0 V when the monitor meter is adjusted to read 0.9 V.

3.3 Power supply voltage

Pre-set potentiometer P2 is used to set the –15 V supply rail which automatically sets to +60 V rail also. P3 adjusts the +15 V rail.

4. MAINTENANCE

4.1 Replacement of modules or components

The schematic diagram giving component part details shows the wiring of the instrument outside the printed circuit board modules. Component part details are shown on the individual circuit diagrams of the modules. Physical layout diagrams of the complete instrument provide identification of modules. Printed circuit board modules are fitted with connectors to facilitate easy replacement.

For replacement and easy repair printed circuit board layout diagrams show copper circuitry in relation to the components. The module terminations are numbered and colour coded to ensure correct lead connection after board replacement.

The amplitude control module is incorporated in an epoxy resin housing and is unrepairable.

4.2 Distortion consequent upon component replacement

In order to obtain the desired very low distortion characteristics of the LDO3 selection of component parts is necessary in the manufacture of the instrument. If a failure occurs it is advantageous to replace a complete module with a factory tested item rather than replace a component part. However, as this may not always be practicable the following notes are included.

A specification requirement for the LDO3 is 0.002% total harmonic distortion in the middle audio frequency band but is typically less than 0.001%. With selection of components some oscillators have a distortion output which cannot be detected below the noise in total harmonic distortion measuring apparatus. (Less than 0.0005%) Distortion may be introduced by the following components:

Fig. 2 Layout. Original design. Plan view

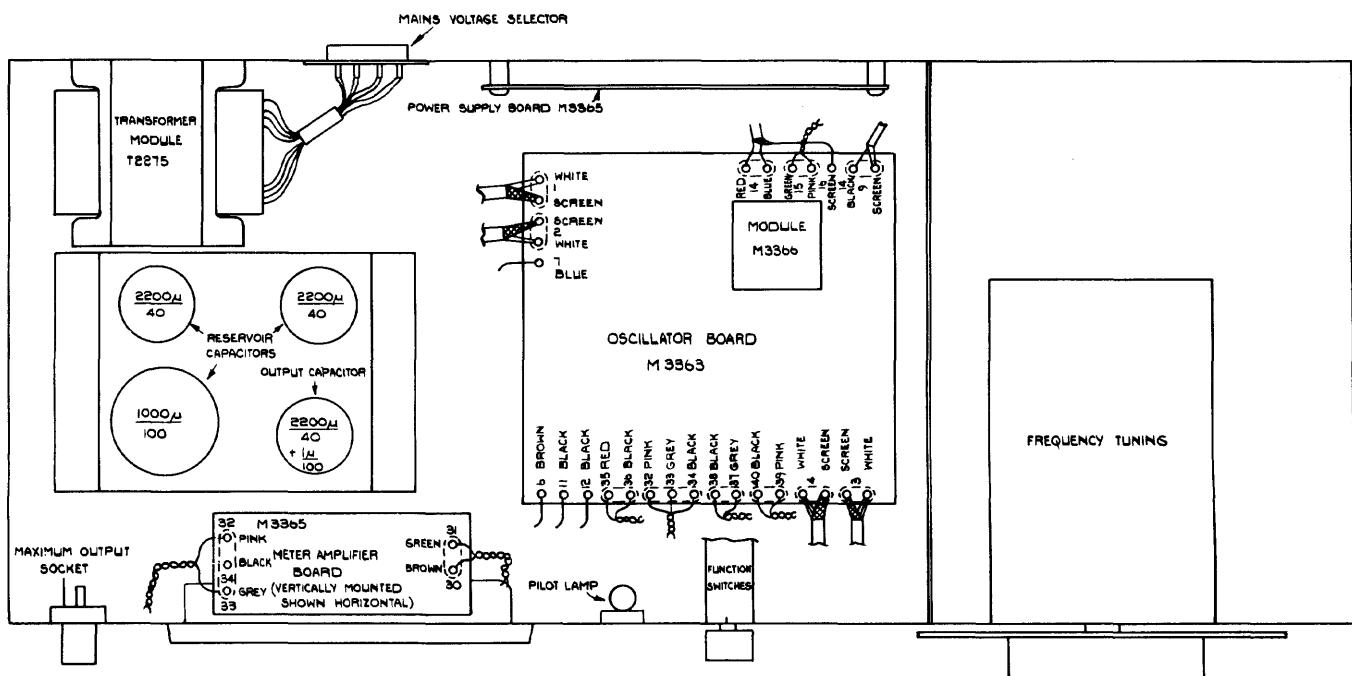


Fig. 2b Layout. Modified design. Plan view

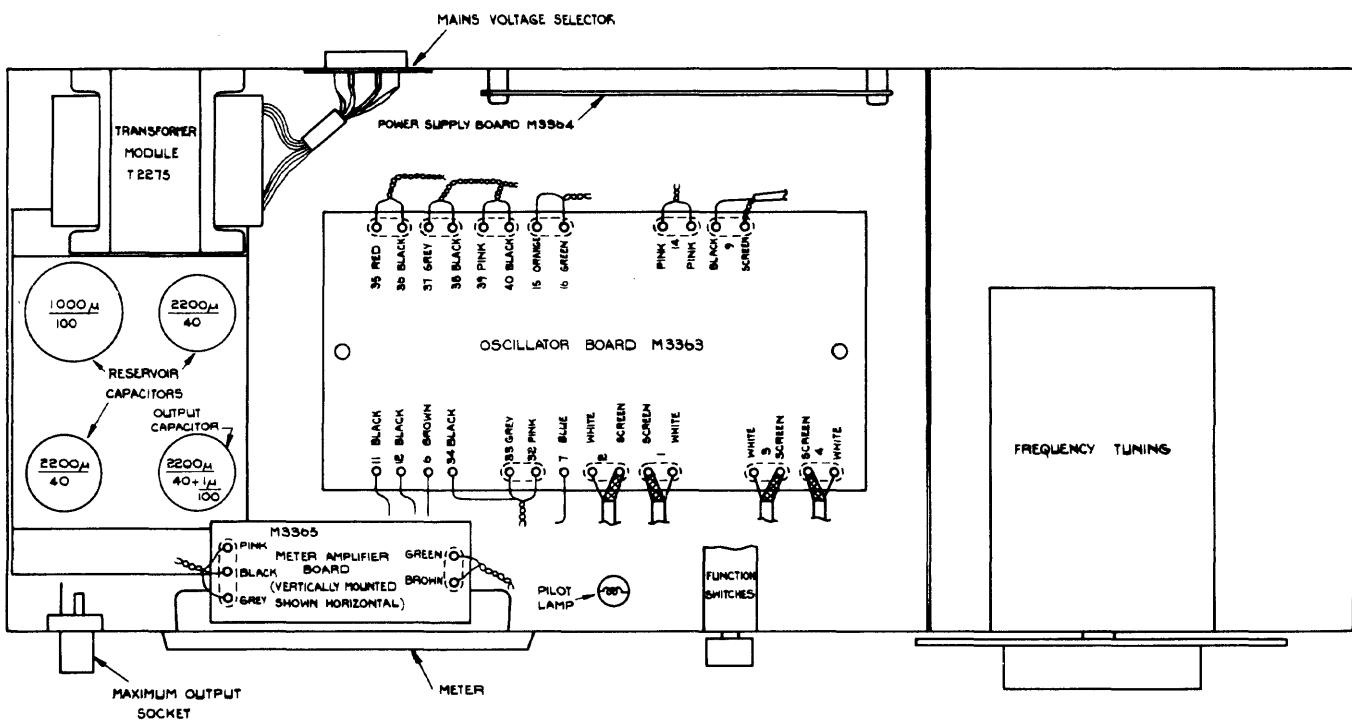


Fig. 3a Layout. Underside

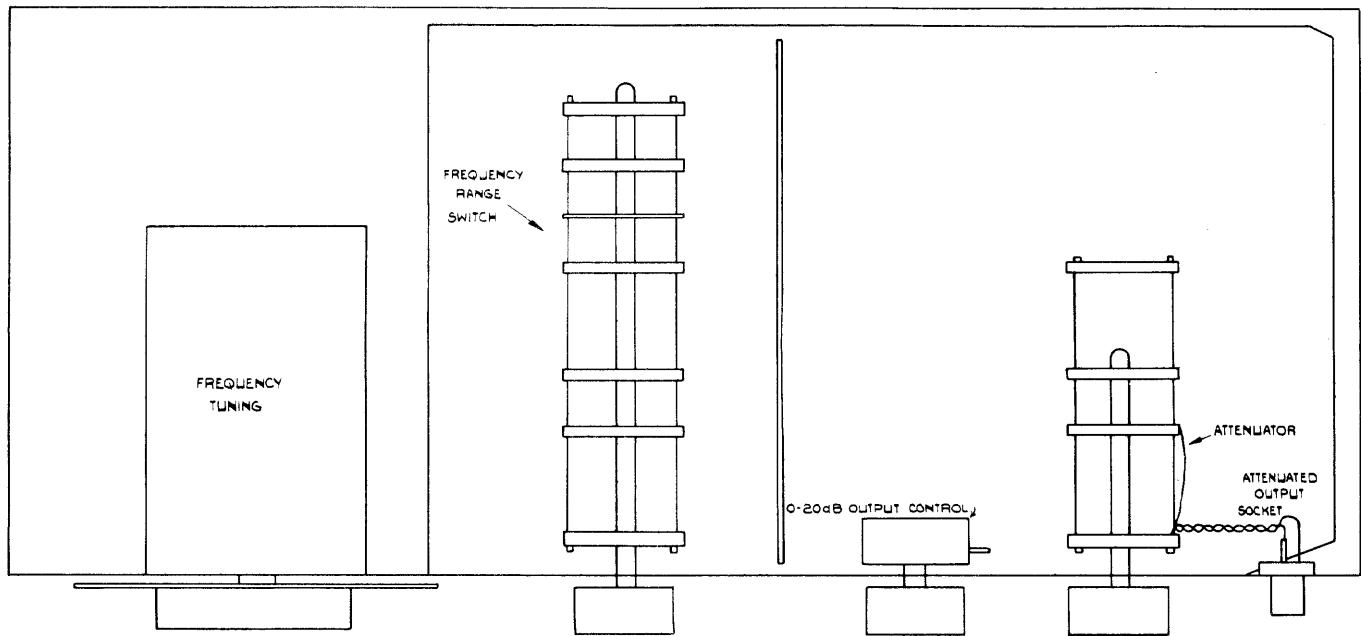


Fig. 3b Layout. Underside (Floating output model)

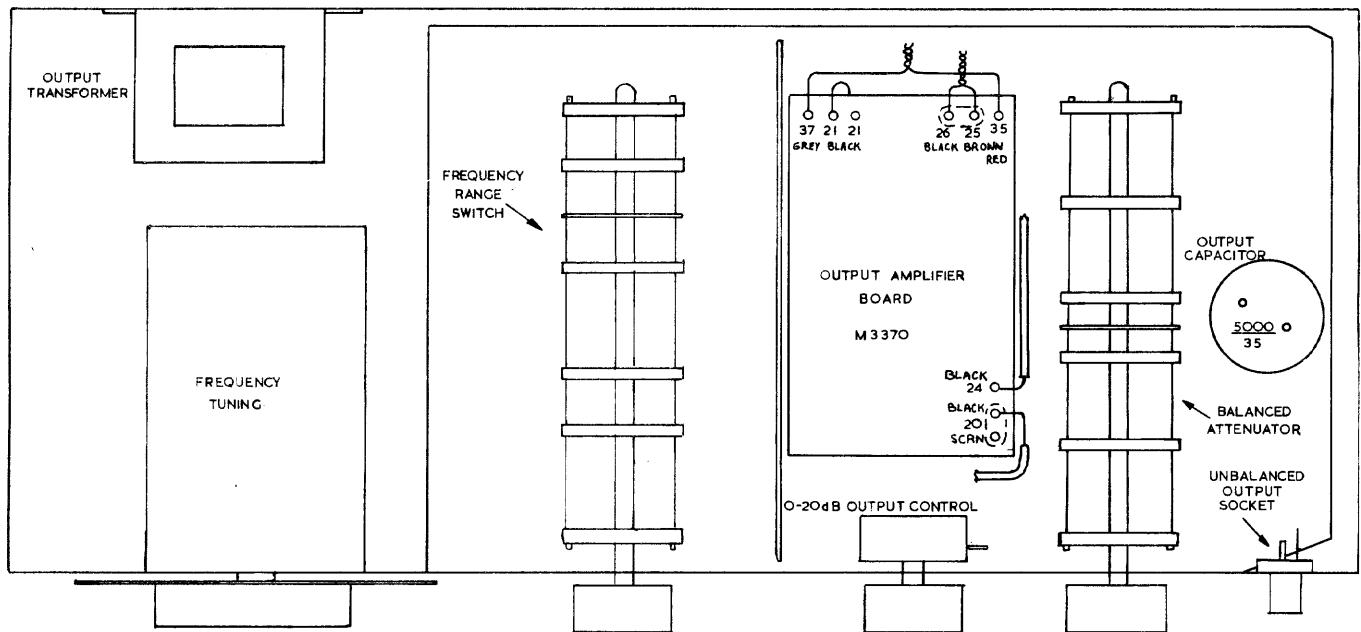


Fig. 4 Layout. Rear panel inside view

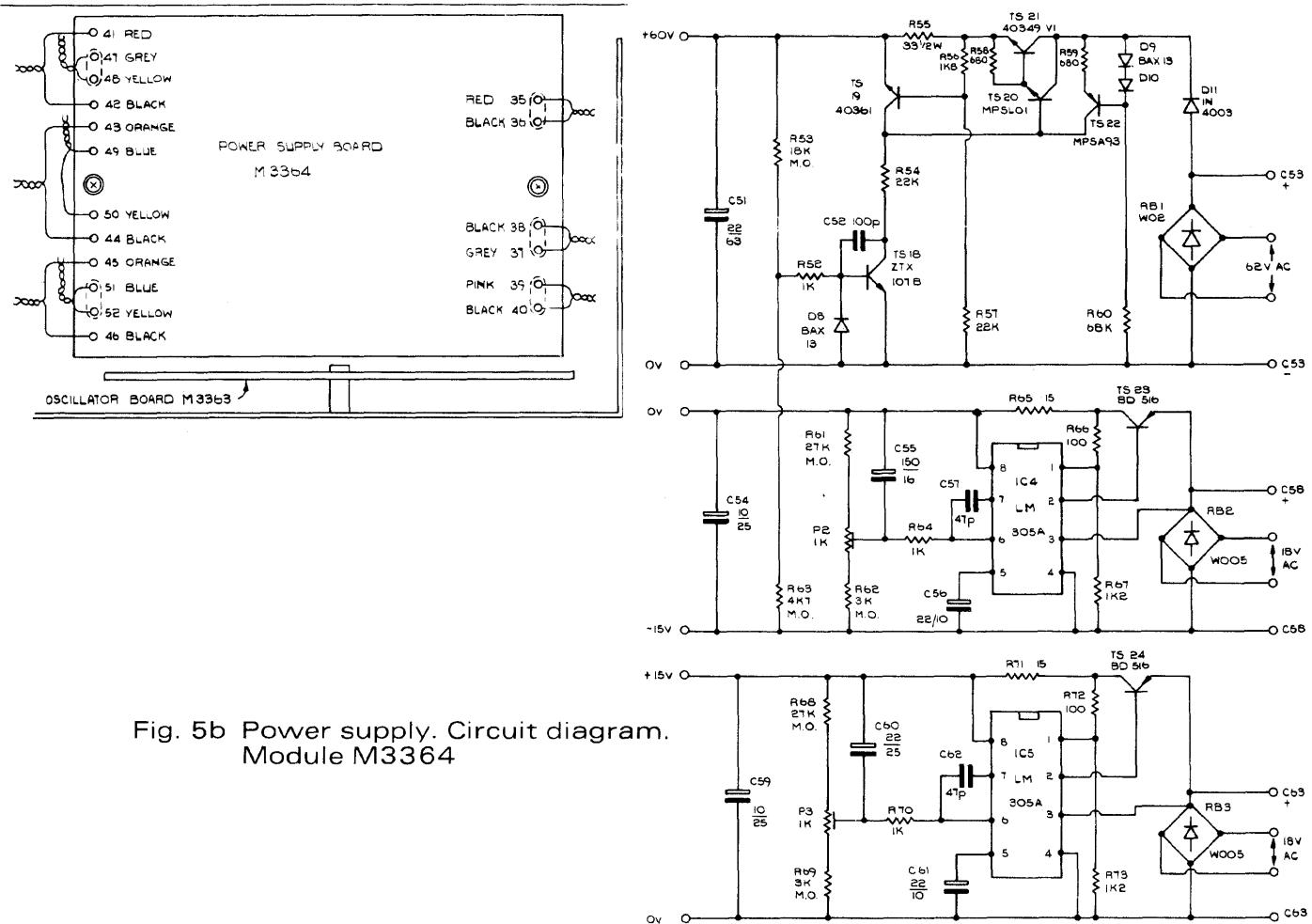


Fig. 5b Power supply. Circuit diagram.
Module M3364

Fig. 5a Power supply. Component layout.
Module M3364

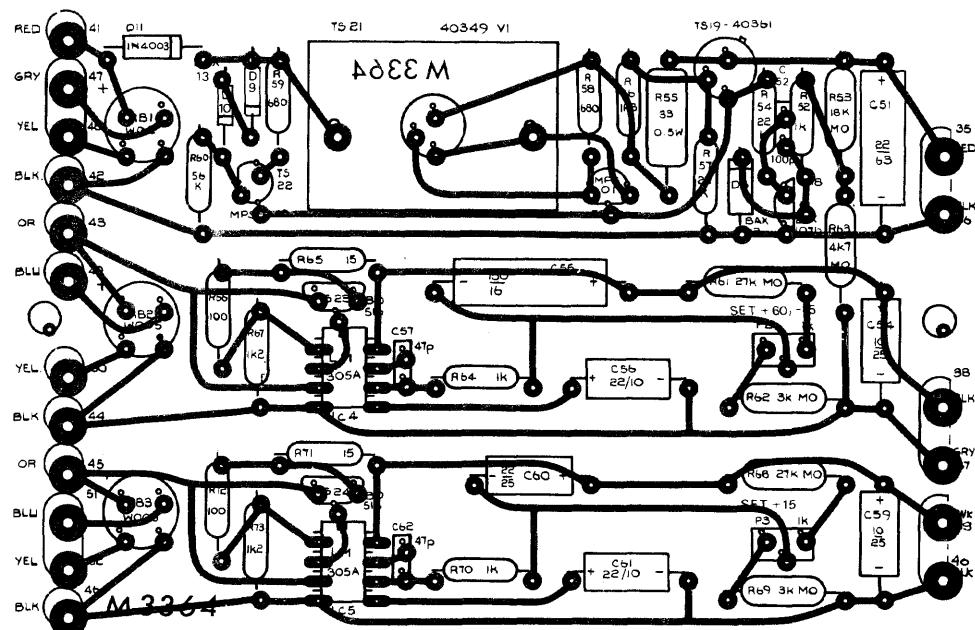


Fig. 6a Transformer amplifier. Component layout. Module M3370

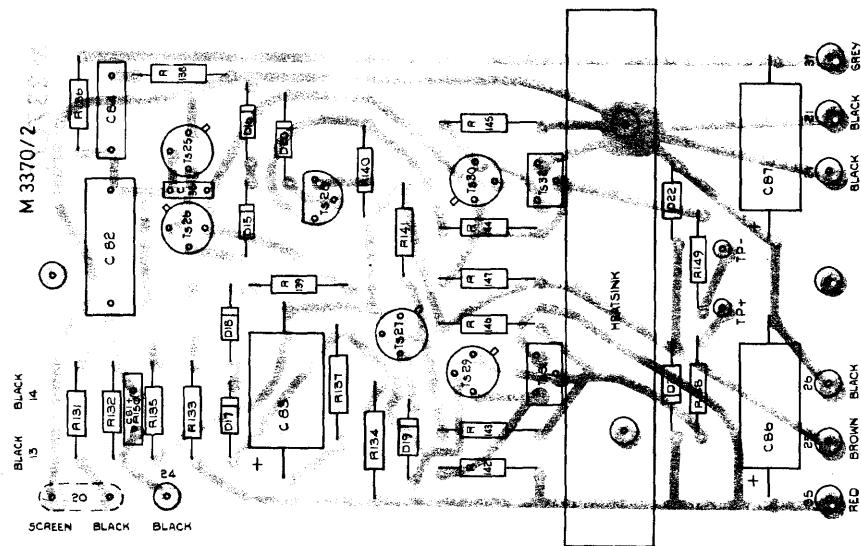


Fig. 6b Transformer amplifier. Circuit diagram. Module M3370

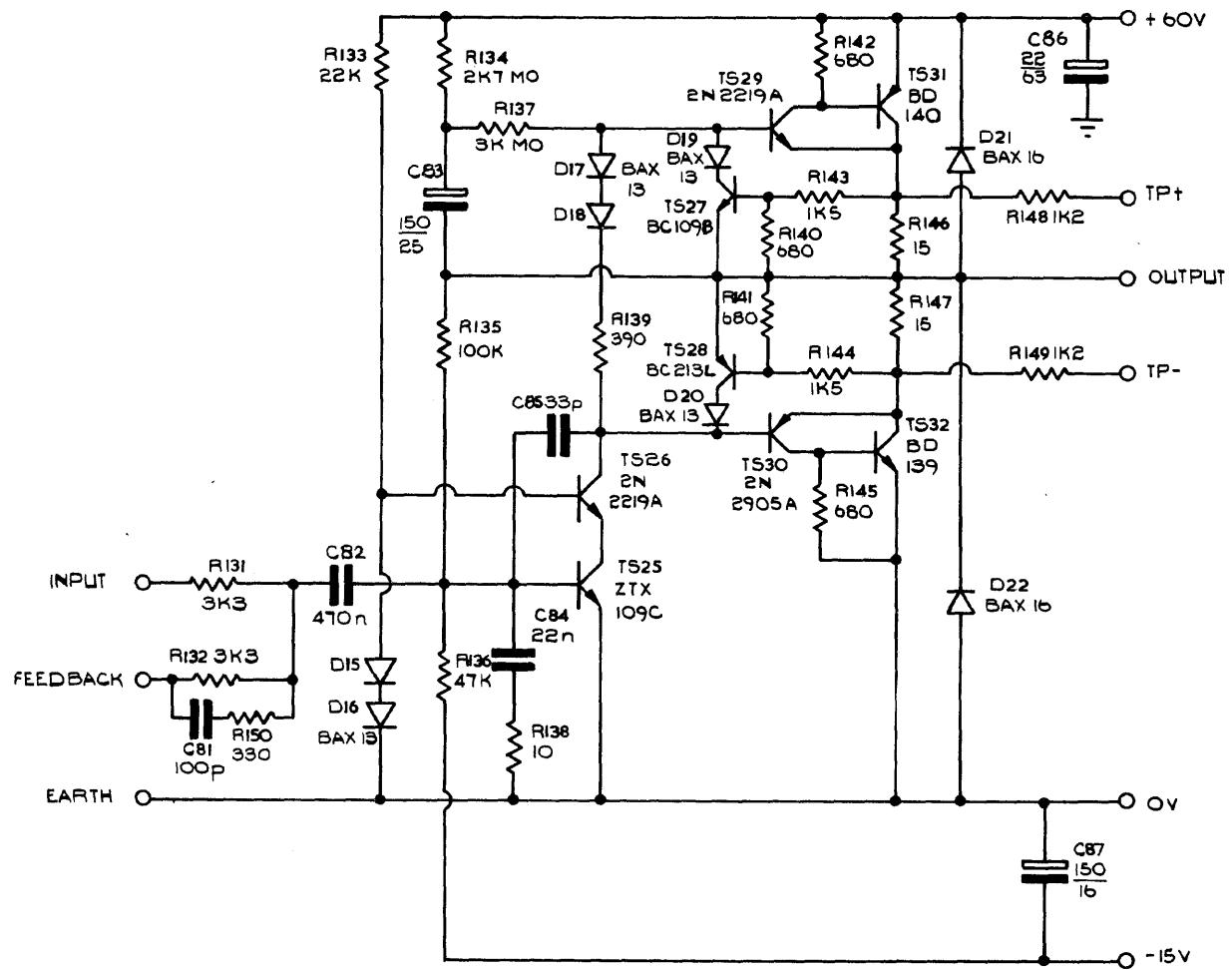
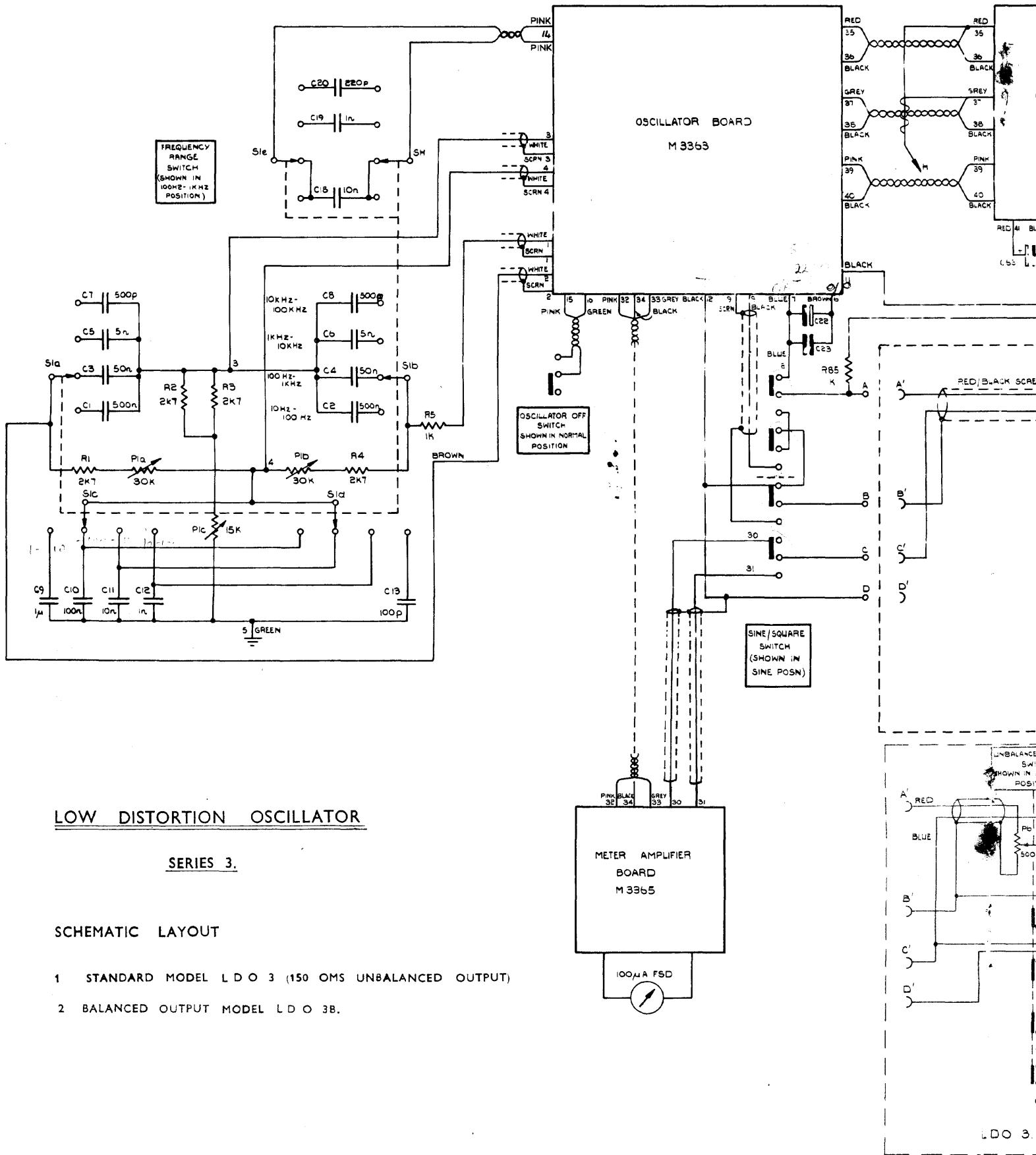


Fig. 1 Schematic diagram



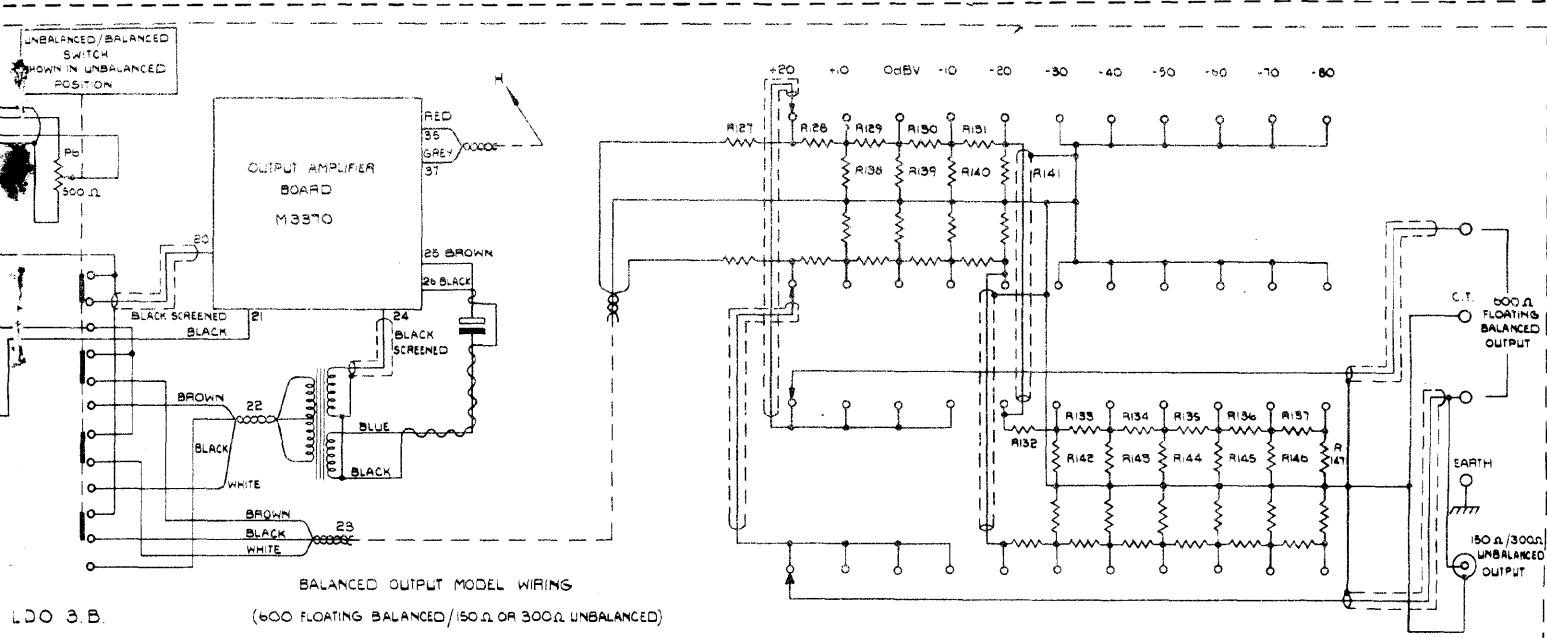
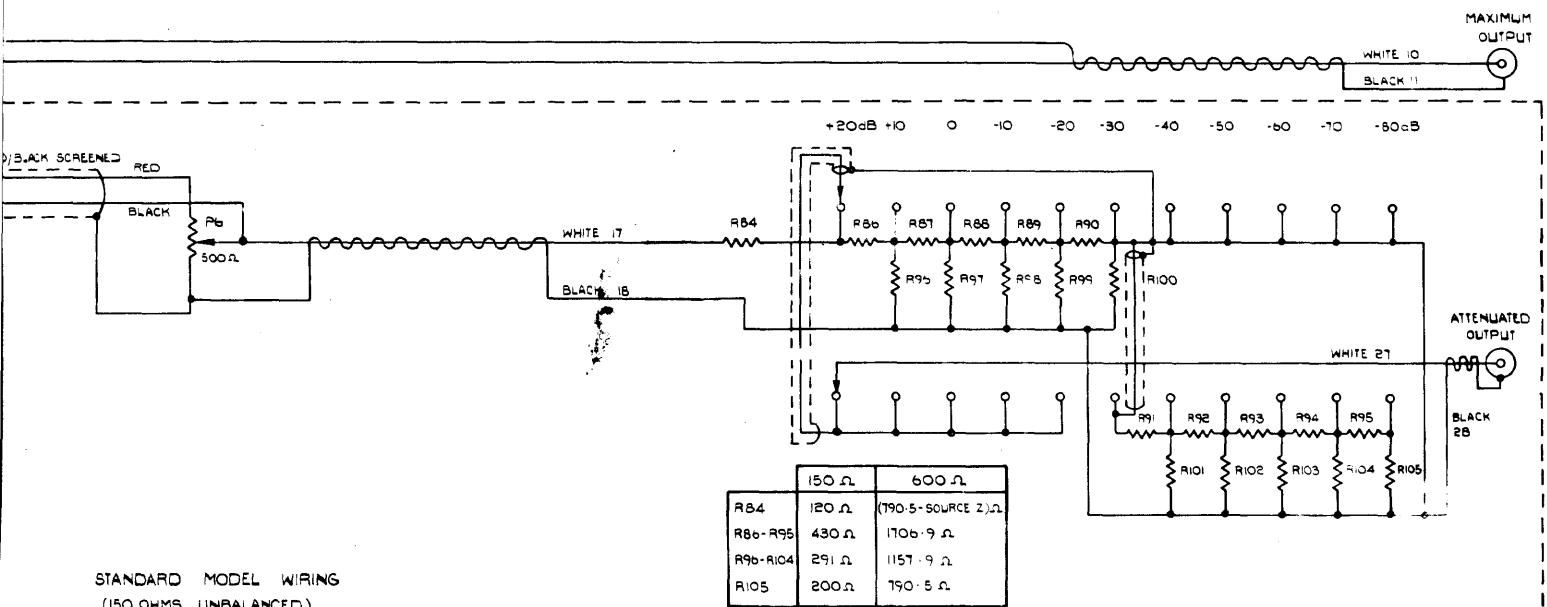
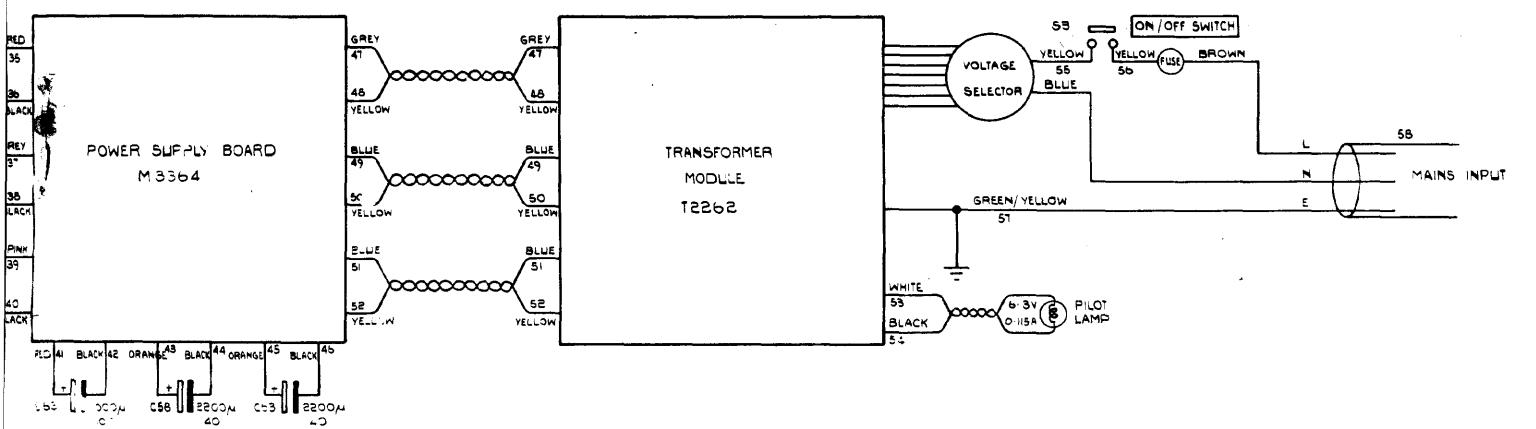


Fig. 1 Schematic diagram

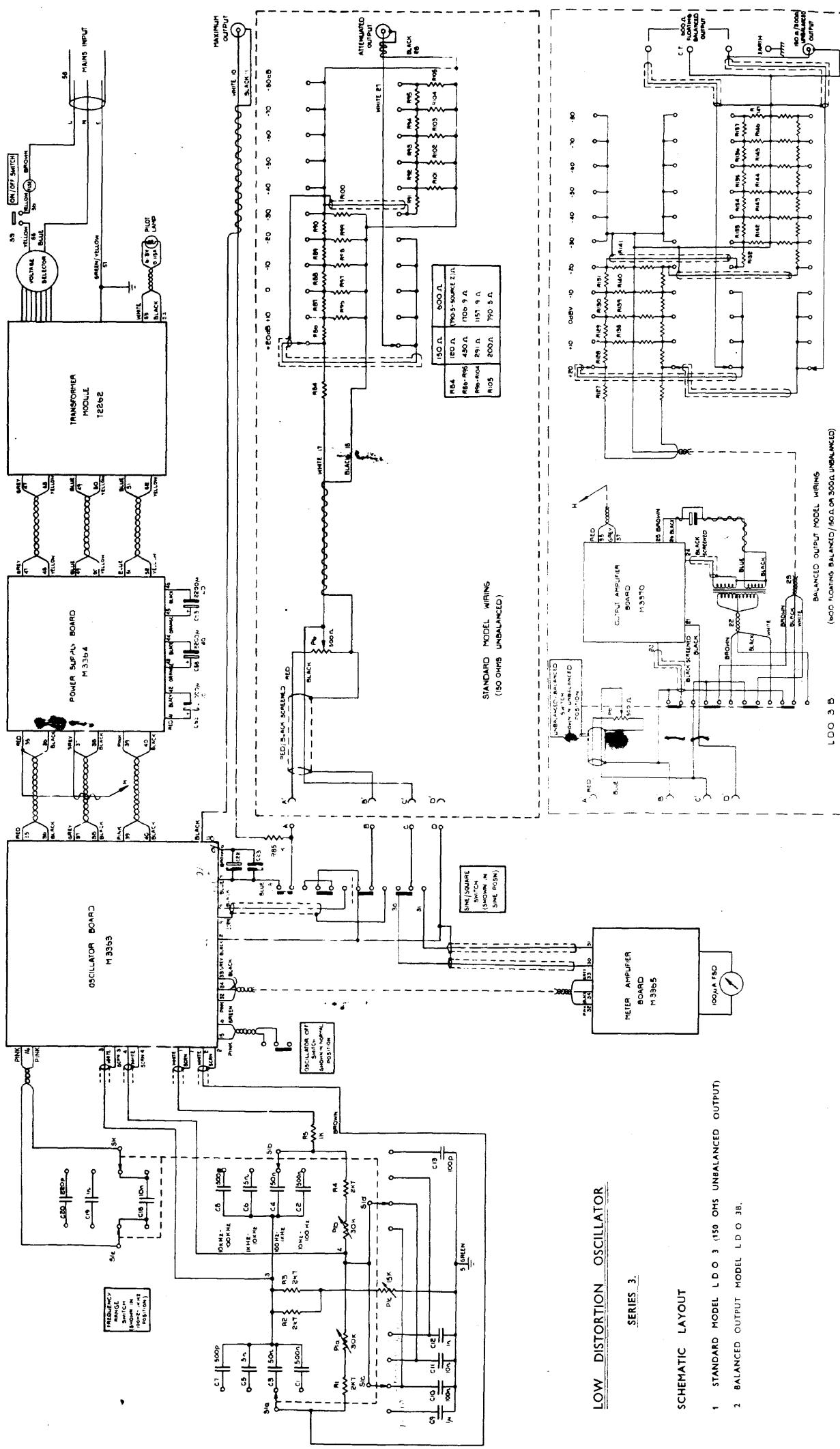


Fig. 7a Oscillator amplifier and limiter.
Component layout. Module M3363/2

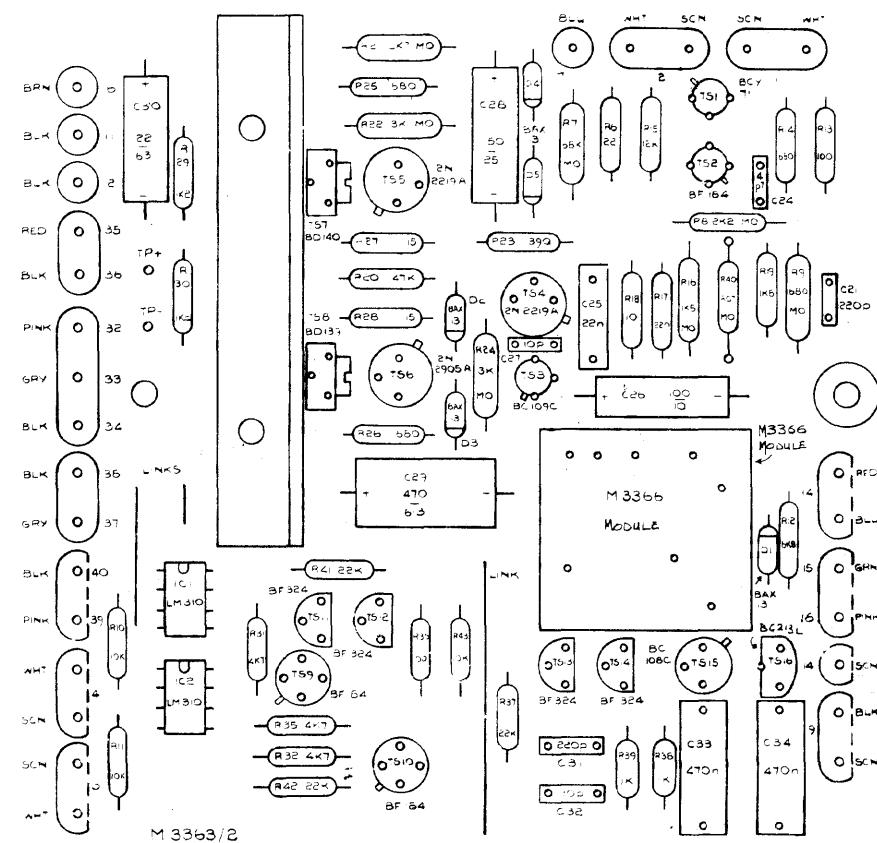


Fig. 7b Oscillator amplifier and limiter.
Component layout. Module M3363/4

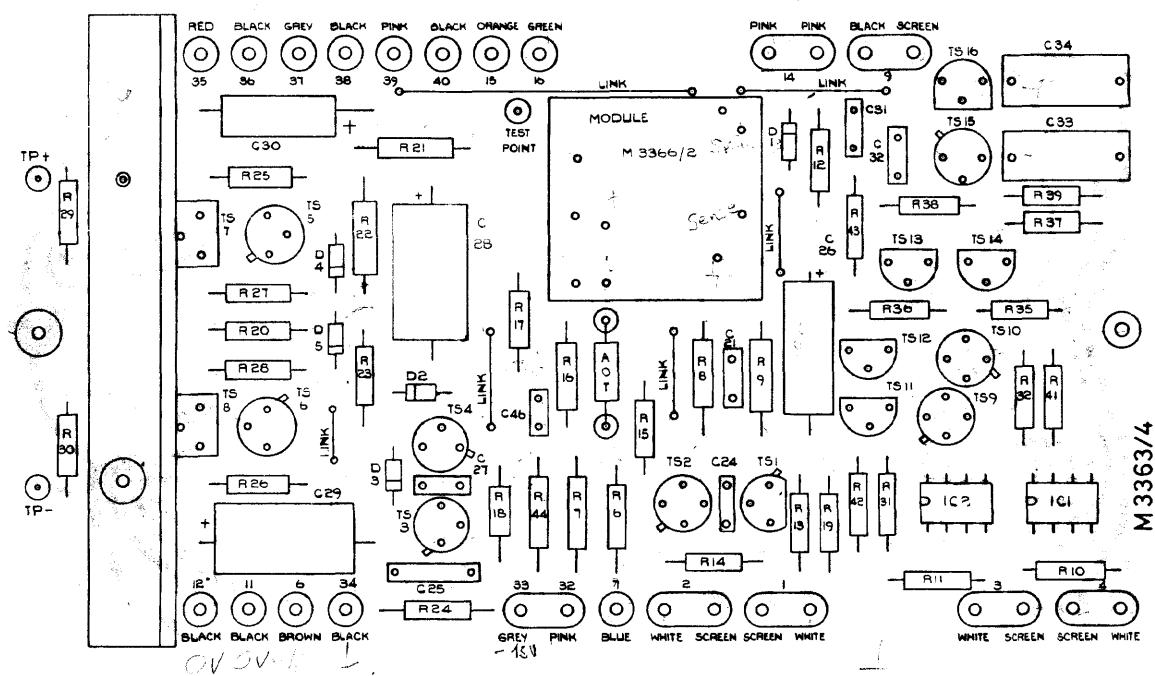


Fig. 7c Oscillator amplifier. Circuit diagram.
Module M3363

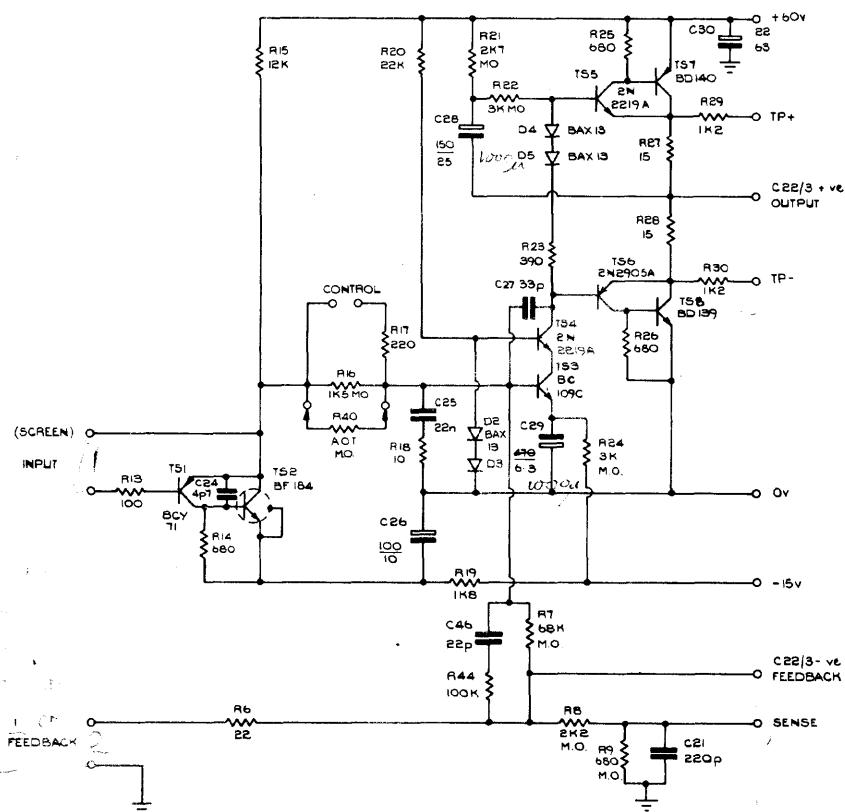


Fig. 7d Limiter. Circuit diagram.
Module M3363

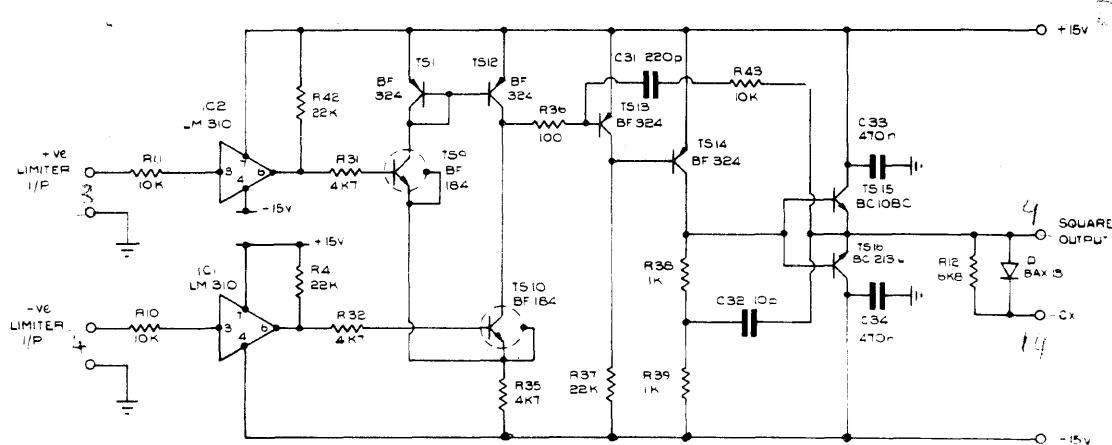


Fig. 8a Meter amplifier. Component layout
Module M3365

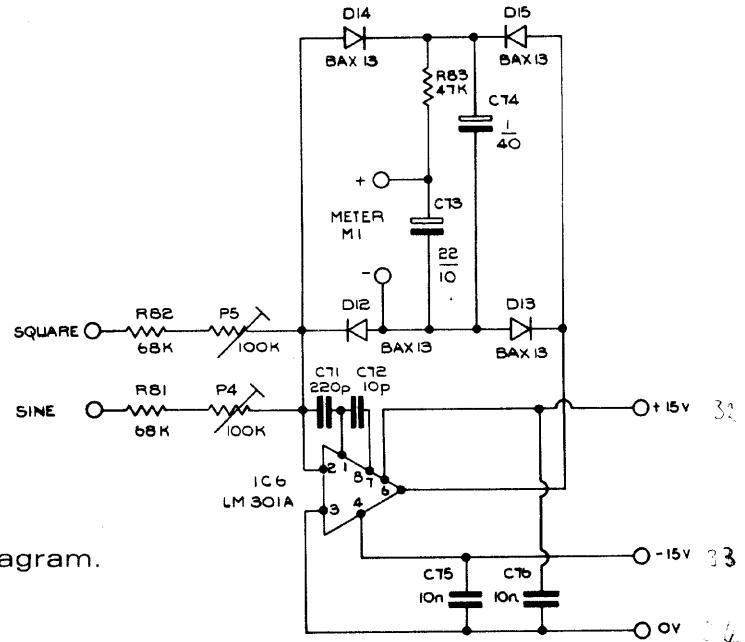
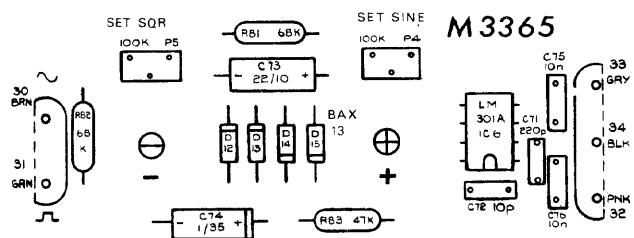


Fig. 8b Meter amplifier. Circuit diagram.
M3365

Low Distortion Oscillator LDO3.B. Balanced output model

1. General

The Low Distortion Oscillator LDO3.B is a standard LDO3 additionally fitted with an output transformer to provide a floating (isolated) output of 600 ohms source resistance balanced about a centre tap. The three output terminations are connected to press type terminals, any of which may be connected to an adjacent terminal at earth (chassis) potential. By suitable external connections the following output arrangements may be obtained.

1. 600 ohms. Floating; balanced to centre tap, floating or earthy; unbalanced.

2. 300 ohms. Floating; unbalanced.

3. 150 ohms. Unbalanced.

The diagram, Fig. 9, shows the wiring from the attenuator to the terminals and illustrates the various output connections. The unbalanced output (BNC) socket must be left unconnected when using balanced output.

2. Performance characteristics, 600 ohms output**2.1 Distortion**

Typical distortion characteristics against frequency are shown in Fig. 10.

2.2 Frequency response

Typical frequency response characteristics are shown in Fig. 11.

2.3 Balance

Typical balance figures for the 600 ohm output are:

1kHz – 80dB, 10kHz – 60dB

The method of measurement is shown in Fig. 12.

3. Overall schematic of LDO3.B

In the LDO3.B the 150 ohm standard model output wiring (shown in the black dotted rectangle, Fig. 1) is replaced by the 600 ohm output wiring (shown in the blue dotted rectangle, Fig. 1.)

Fig. 10 Typical distortion characteristic
LDO3.B.

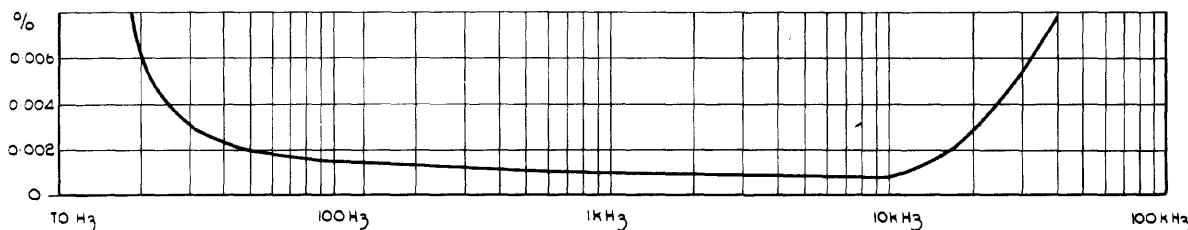


Fig. 11 Typical frequency response
characteristic LDO3.B.

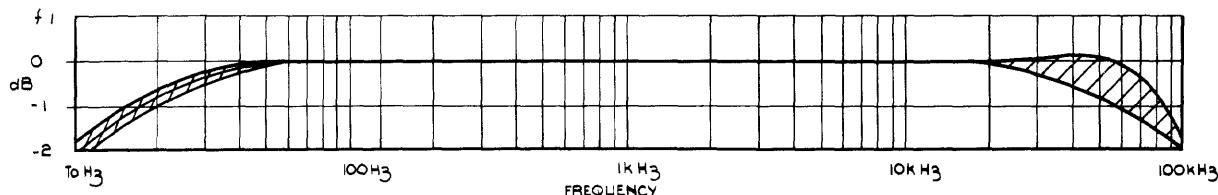


Fig. 9 Output schematic

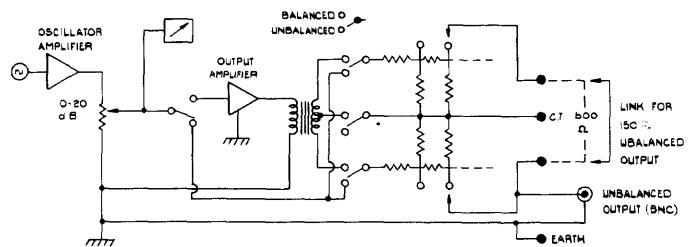
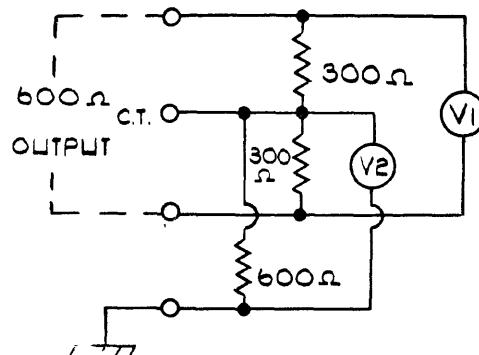


Fig. 12 Output balance test.



$$\text{Balance} = 20 \log_{10} \frac{V_1}{V_2}$$

Component: 2N2219A transistor. Circuit reference TS4

This is the main contributor to distortion in the oscillator. Some manufacturers' transistors are better than others and vary from batch to batch. It should be noted that some makes of 2N2219A are unsuitable because of squeegging when the oscillator is tuned to frequencies above 60 kHz, although they satisfy the condition of low distortion below the squeegging frequency.

Component: BC109C transistor, circuit reference TS3

Distortion may also be introduced by this transistor but the incidence is small compared with TS4.

Components: 0.47 µF and 0.047 µF. Circuit reference C1 and C3

Capacitors C₁, C₃, C₅ and C₇ in the feedback circuit from the oscillator amplifier have a higher voltage applied to them than components in the other branches of the twin T circuit. A non-linearity in the voltage/current relationship in the capacitor will produce distortion. In practice it has exhibited itself on the two low frequency bands controlled by C₁ and C₃. 100 V rated capacitors have a considerably higher yield than 63 V capacitors for low distortion, although the applied voltage is approximately 12 V r.m.s.

Component: 500 ohm potentiometer. Circuit reference P6

Initially cermet potentiometers were used in this position, subsequently replaced by a moulded track type, and now wire wound.

Distortion is introduced by non-linearity of the contact resistance between the slider and the resistance element. It can occur in cermet, moulded carbon and wirewound controls but less frequent in wirewound types. It can be corrected in W.W. types by adjustment to provide a lower contact resistance between slider and element. The fault exhibits itself in a higher distortion reading when P6 (0–20 dB attenuator) is adjusted to approximately 20% of maximum of output, than when set for maximum output. If cleaning the wiper and track with a solvent and increasing the contact pressure does not eliminate the distortion it should be replaced.

Should you require advice or the supply of any component, module or sub assembly please write to the Service Department, Hedley Radford Instruments Bristol BS3 2HZ, England, or in the case of countries outside the U.K. to the Radford agent/importer.

5. GUARANTEE

Home

This instrument is guaranteed for a period of one year from the date of purchase. It covers the free replacement or repair of any defective component or part of the equipment during this period. It also covers the cost of labour in executing the repair or replacement if the instrument is returned to the factory service department, carriage paid, within the guarantee period.

Overseas

It is generally not practicable to return the instrument to the factory in England and instruments will normally be maintained in a serviceable condition by the replacement of sub-assemblies, modules or specific component parts.

In the case of instruments purchased through an Agent in a country outside the U.K., the agent will act for the manufacturer in that country. Service enquiries should therefore be directed to the Agent.

If the instrument is purchased direct from the factory in countries where there is no Radford Agent, enquiries should be directed to the Service Department in Bristol.

Hedley Radford Instruments

Diagrams

Fig. 1 Schematic diagram

Fig. 2 Layout. Original design. Plan view

Fig. 2b Layout. Modified design. Plan view

Fig. 3a Layout. Underside

Fig. 3b Layout. Underside (Floating output model)

Fig. 4 Layout. Rear panel inside view

Fig. 5a Power supply. Component layout. Module M3364

Fig. 5b Power supply. Circuit diagram. Module M3364

Fig. 6a Transformer amplifier. Component layout. Module M3370

Fig. 6b Transformer amplifier. Circuit diagram. Module M3370

Fig. 7a Oscillator amplifier and limiter. Component layout. Module M3363/2

Fig. 7b Oscillator amplifier and limiter. Component layout. Module M3363/4

Fig. 7c Oscillator amplifier. Circuit diagram. Module M3363/4

Fig. 7d Limiter. Circuit diagram. Module M3363/4

Fig. 8a Meter amplifier. Component layout Module M3365

Fig. 8b Meter amplifier. Circuit diagram. M3365

Fig. 9 Output schematic

Fig. 10 Typical distortion characteristic LDO3.B.

Fig. 11 Typical frequency response characteristic LDO3.B.

Fig. 12 Output balance test.

Bristol BS3 2HZ England

Fig. 4 Layout. Rear panel inside view

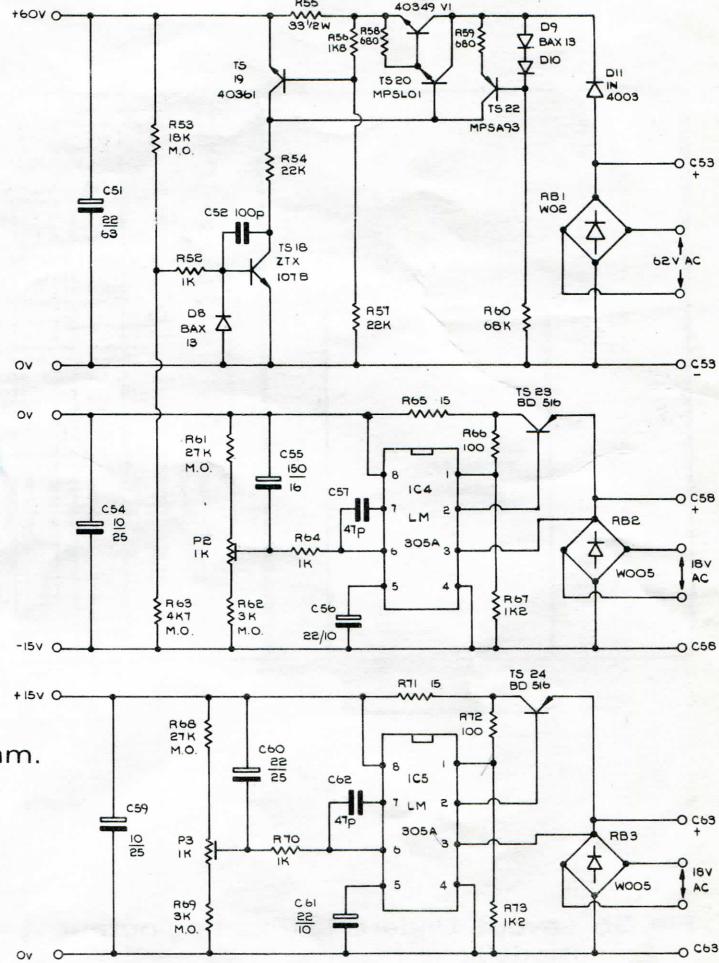
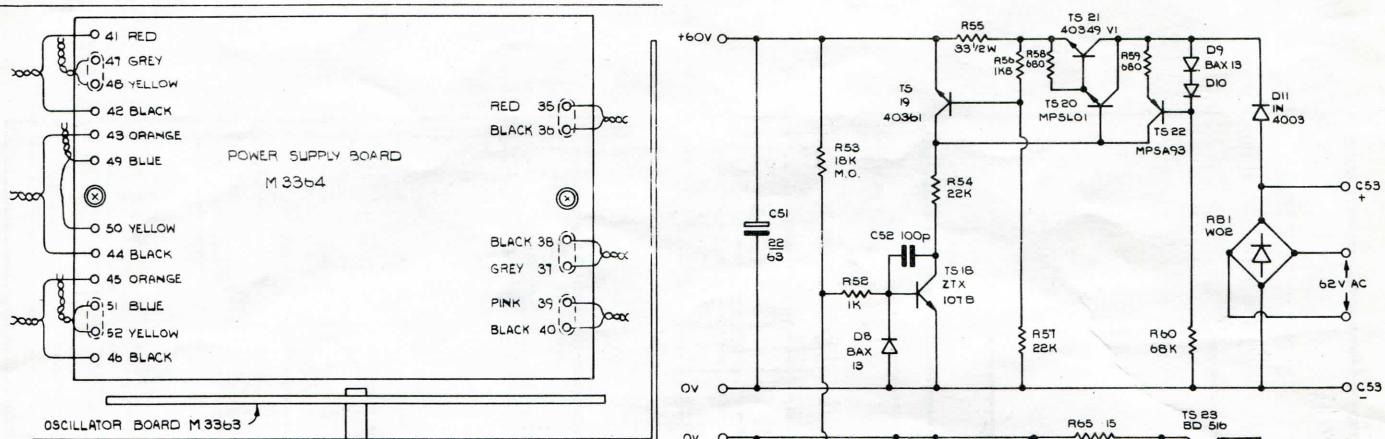


Fig. 5b Power supply. Circuit diagram.
Module M3364

Fig. 5a Power supply. Component layout.
Module M3364

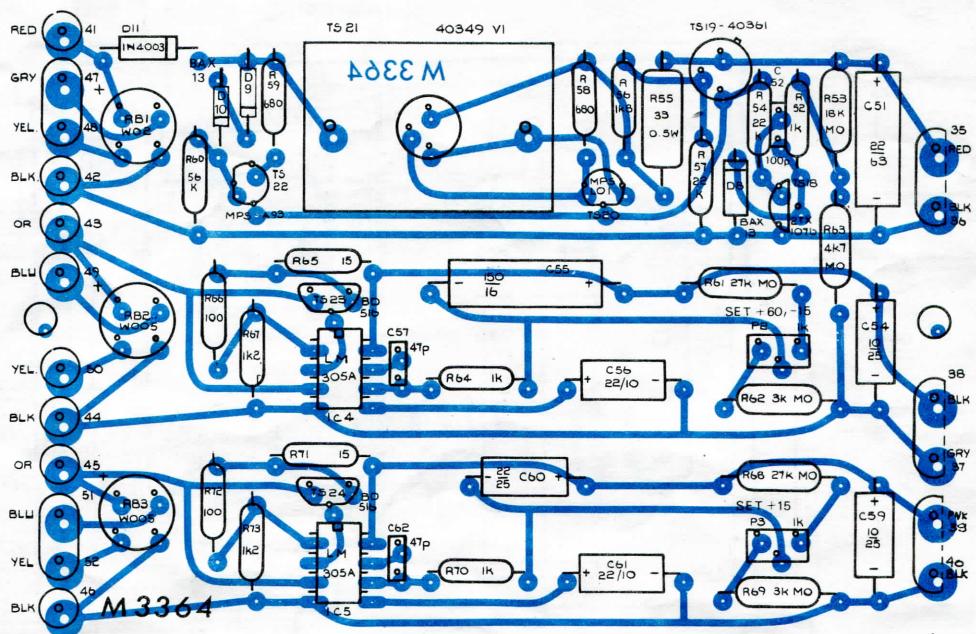


Fig. 6a Transformer amplifier. Component layout. Module M3370

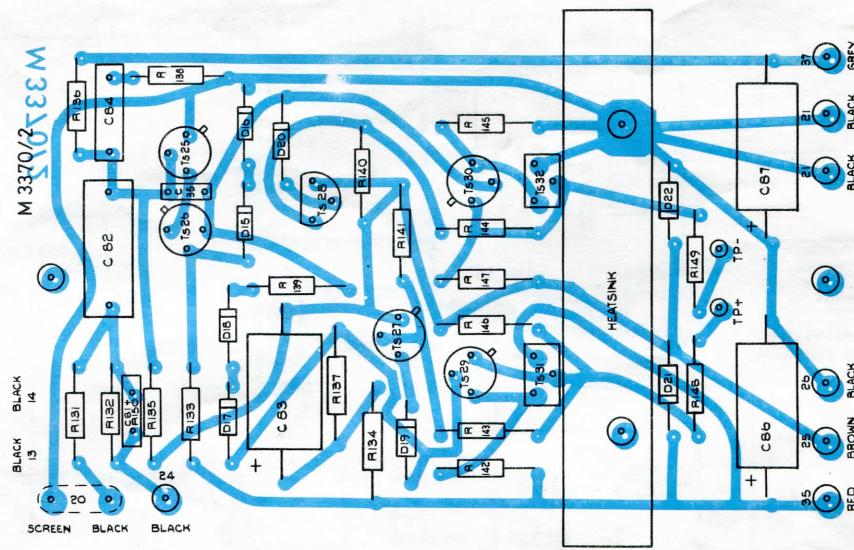


Fig. 6b Transformer amplifier. Circuit diagram. Module M3370

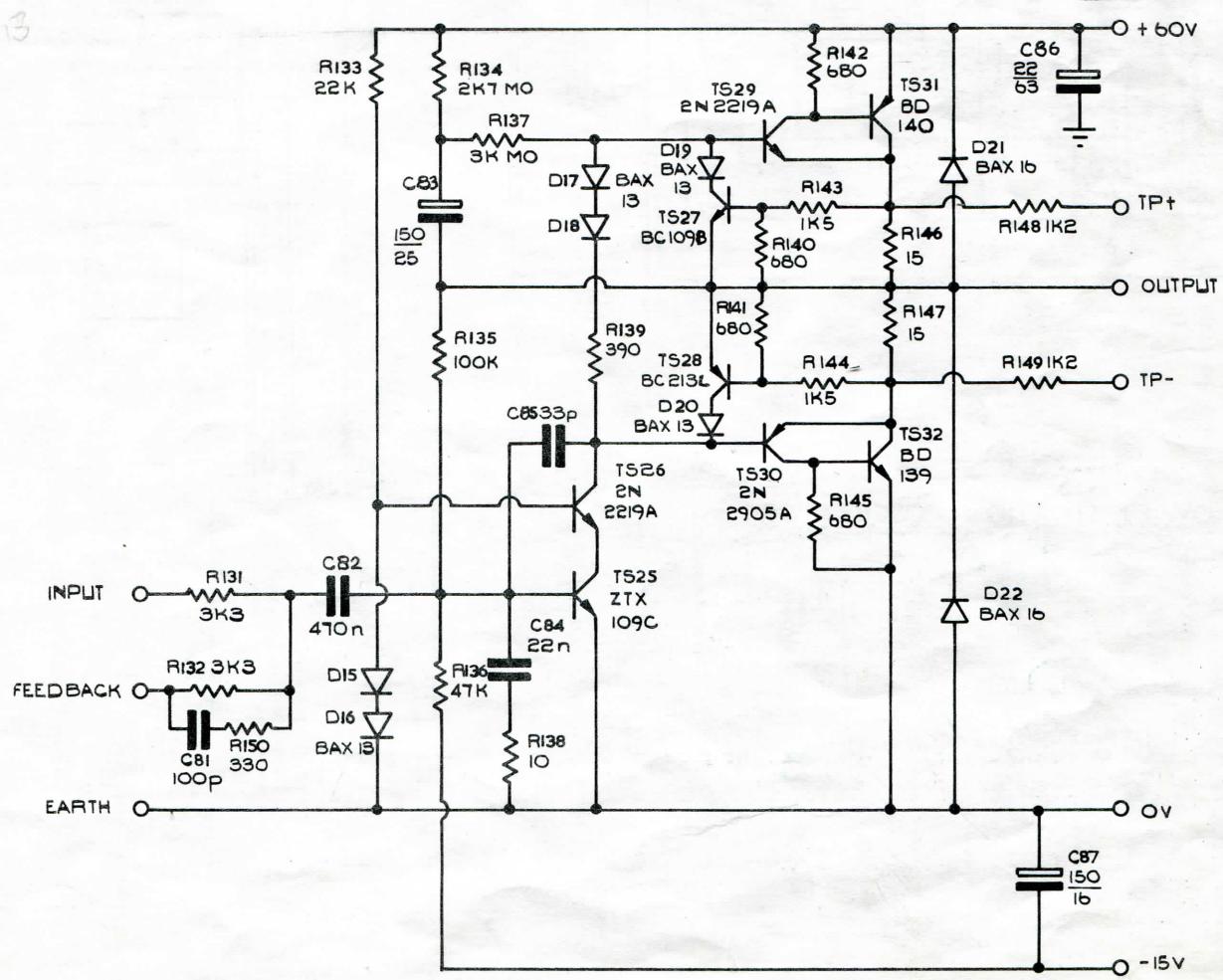


Fig. 7a Oscillator amplifier and limiter.
Component layout. Module M3363/2

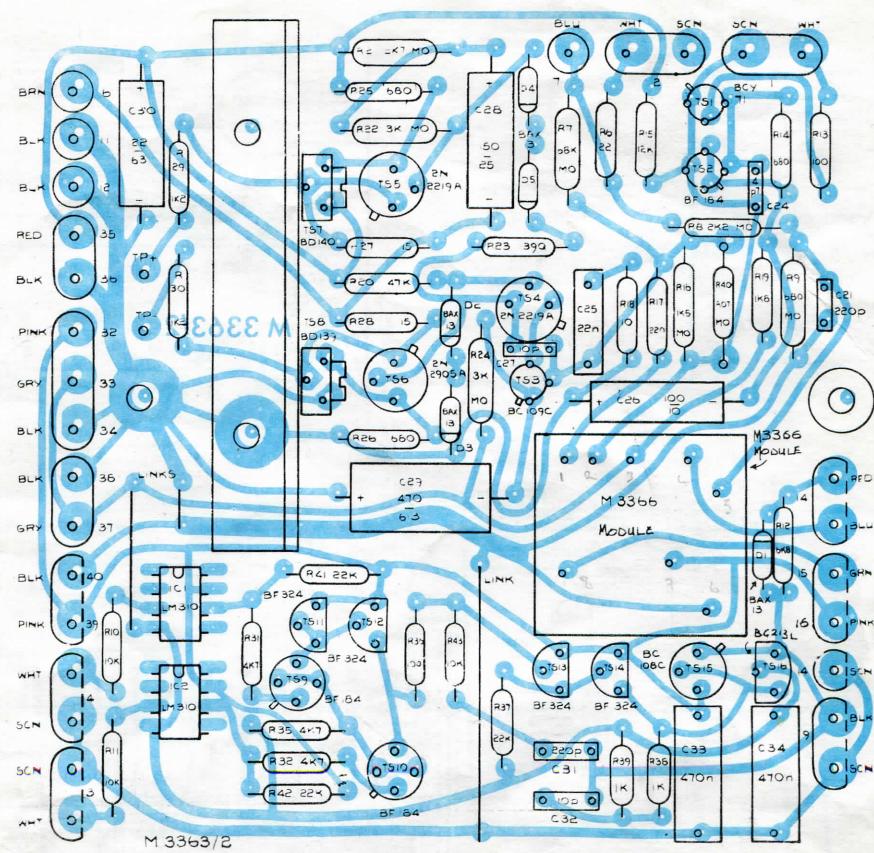


Fig. 7b Oscillator amplifier and limiter.
Component layout. Module M3363/4

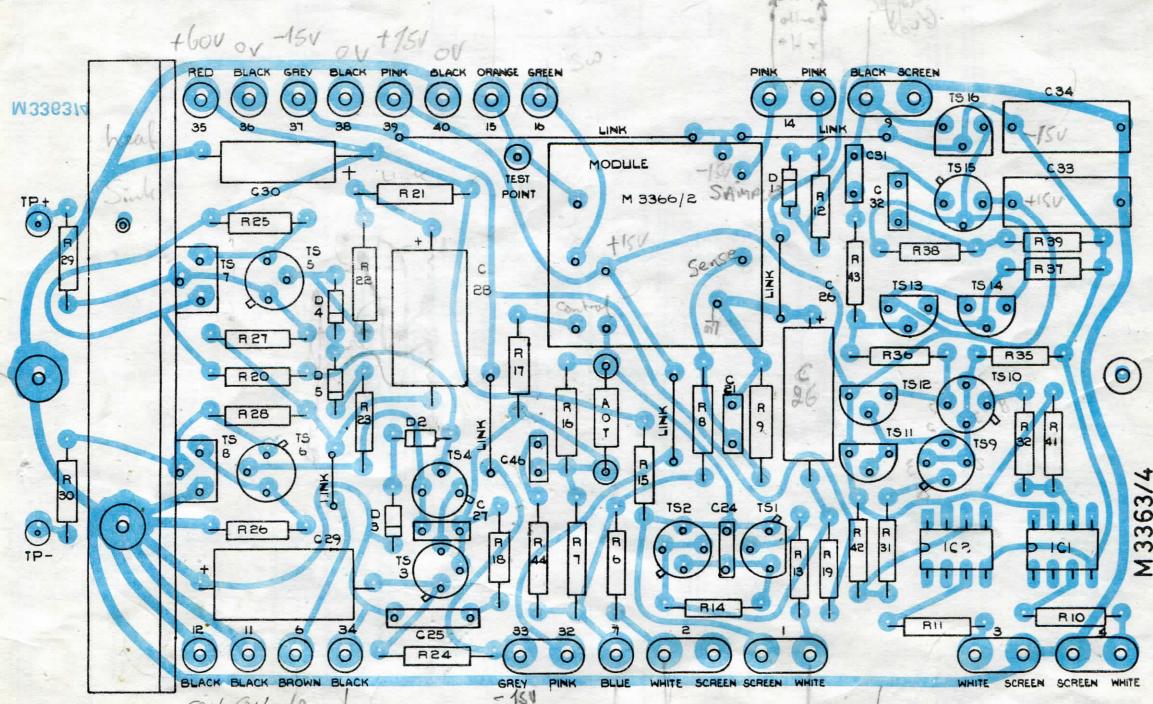


Fig. 8a Meter amplifier. Component layout
Module M3365

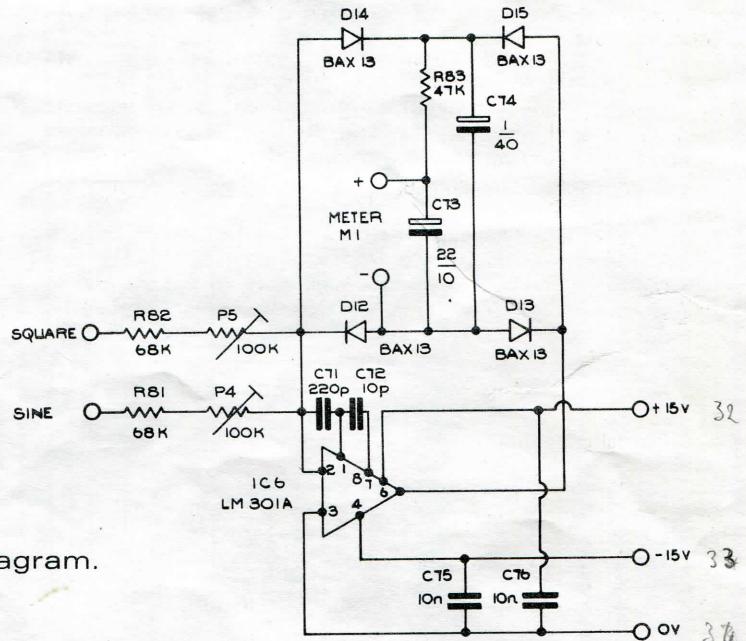
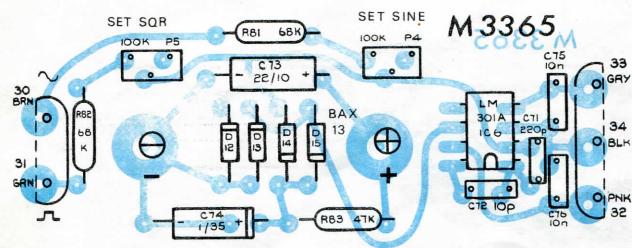


Fig. 8b Meter amplifier. Circuit diagram.
M3365

Woodside Electronics

Proprietor: J.H. Widgery

Kimberley Road

Clevedon

Avon BS21 6QJ

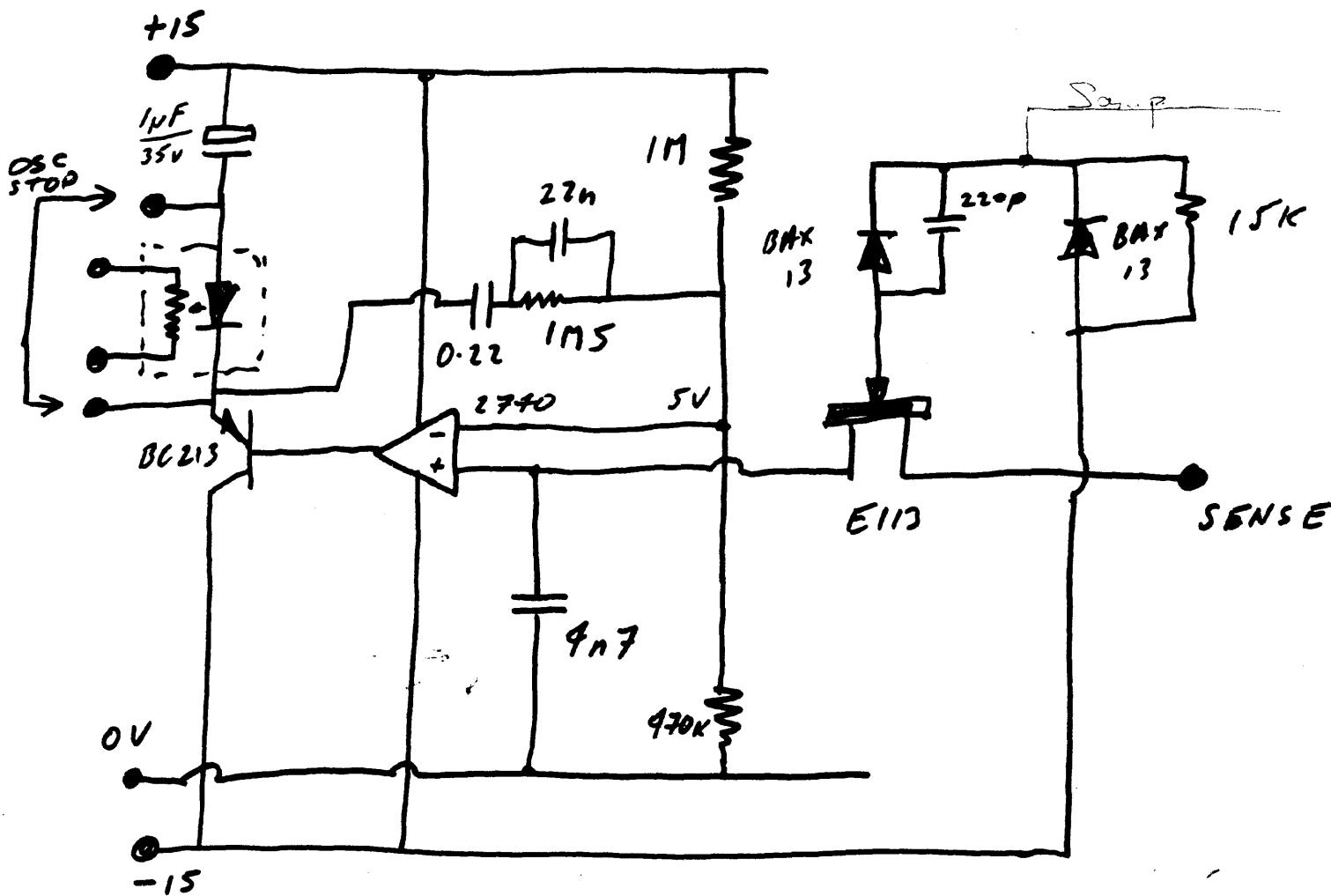
Telephone: 0272 877611

10-10-86

Att Mr. Jim Anderson

Radford 1003

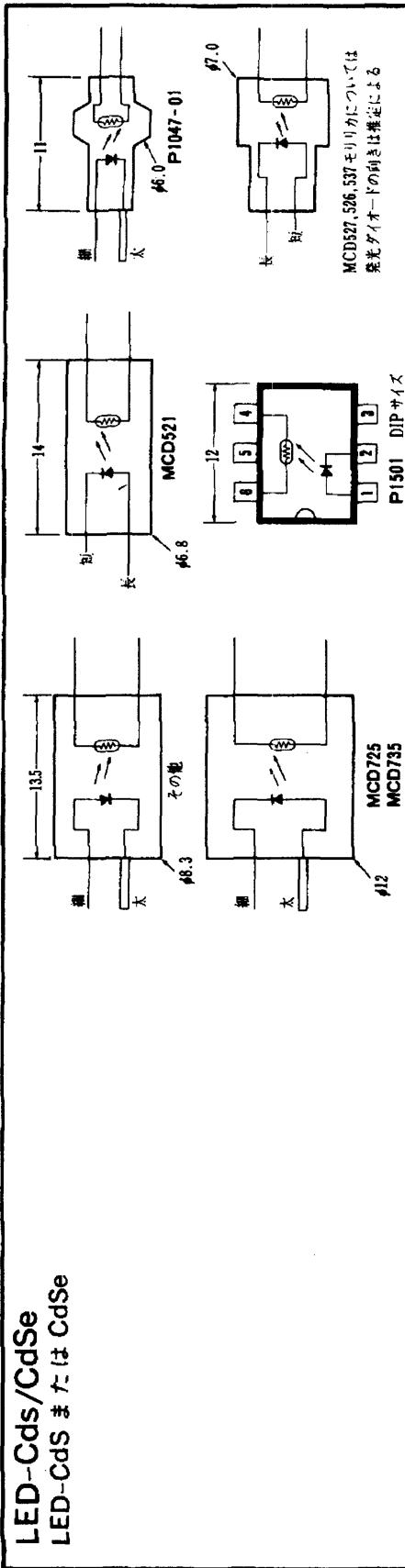
A.C.T. M3366/2



OPTO COUPLER MCDS521

John Widgery

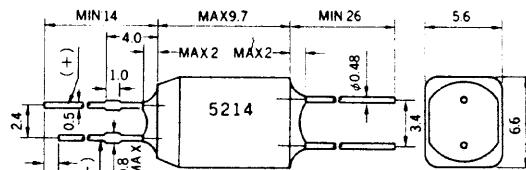
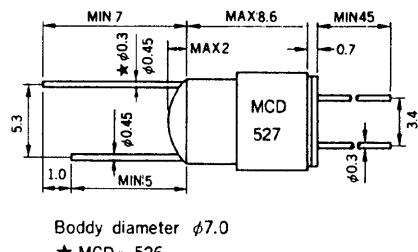
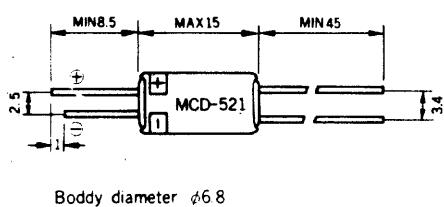
LED-Cds/CdSe
LED-CdS または CdSe



MCD527, 536, 537モリリカについては
発光ダイオードの向きは基準による

型 番	社 名	最 大 定 格										作 動 特 性						備 考
		1 次 電 圧		2 次 電 圧		全 電 圧		1 次 電 流		2 次 電 流		電 圧		電 流		電 圧		
		I_F max (mA)	V_F max (V)	P_D max (mW)	V_R max (V)	I_R max (mA)	P_{DR} max (mW)	I_S max (A)	BV min (kV) DC/AC*	T_g min (°C) typ	V_F max typ (V/mA)	I_T max (V/mA)	dV min (V/μs)	R_{on} max (Ω)	I_{off} min (mA)	I_{FT} max (mA)	t_{off} max typ (μs)	
P873-G35-380	浜松	35	4	100	200	—	—	50	3.0	-30~50	2.0/20*	—	—	—	200~1k	1M	—	
P873-G35-201B	—	—	—	—	—	—	—	—	—	—	—	—	—	—	1k~5k	10M	—	
P873-G35-552	—	—	—	100	—	—	—	—	—	-30~55	—	—	—	—	50~200	1M	—	
P873-G35-687	—	—	—	400	—	—	—	100	—	—	—	—	—	—	200~1k	—	n	
P873-G35-368	—	—	—	—	—	—	—	50	—	-30~50	—	—	—	—	500~3k	10M	—	
P873-18	—	—	—	—	—	—	—	100	—	-30~60	—	—	—	—	200~1k	1M	—	
P1047-01	モリリカ	25	n	75	100	—	—	30	0.5	-30~50	2.0/15*	—	—	—	1.5k	n	n	
P1501	モリリカ	35	5	100	200	—	—	100	2.0	-30~60	2.0/20*	—	—	—	500*	n	—	
MCD321	モリリカ	50	3	90	150	—	—	70	0.8	-20~70	1.8/30*	—	—	—	1k	3M	—	
MCD326	モリリカ	10	4	60	n	—	—	n	3.0	n	1.9/6*	—	—	—	2k	n	2m*	
MCD527	モリリカ	50	2	90	n	—	—	n	n	1.7/30*	—	—	—	—	1k	2M	—	
MCD537	モリリカ	n	n	200	—	—	—	40	n	1.8/30*	—	—	—	—	5k	100M	—	
MCD725	モリリカ	35	4	100	350	—	—	90	n	2.0/20*	—	—	—	—	3k	3M	—	
MCD735	モリリカ	n	n	100	—	—	—	60	n	1.9/15*	—	—	—	—	150	1M	—	
MCD718	モリリカ	40	—	n	250	—	—	72	n	-10~50	2.8/40	—	—	—	26k	2M	—	
MCD728	モリリカ	40	4	100	500	—	—	90	3.0	-20~50	2.1/20*	—	—	—	600	100M	—	
															3m*	8m*		

* 1.15倍
† 1.15倍
‡ 1.15倍
§ 1.15倍
|| 1.15倍
||| 1.15倍
H: 1.15倍
L: 0.902倍



項目 Type	特性 Characteristics	記号 Symbol	MCD-521H MIN. TYP. MAX.	MCD-521L MIN. TYP. MAX.	MCD-527 MIN. TYP. MAX.	MCD-526 MIN. TYP. MAX.	MCD-5214 MIN. TYP. MAX.	単位 Units
入力側 (LED) Input	最大許容損失 Maximum power dissipation	P _{Dm}	90	90	90	23	80	mW
	最大直流順電流 Maximum forward current	I _{FM}	50	50	50	10	40	mA
	推奨順電流 Continuous forward current	I _F	30	30	30	6	20	mA
	順電圧(推奨時) Continuous forward voltage	V _F	1.8	1.8	1.7	1.9	1.65	V
	最大直流逆電圧 Maximum reverse voltage	V _{RM}	3	3	2	4	4	V
出力側 (CdS) Output	ON抵抗(推奨時) ON-resistance (at recommended voltage)	R _{ON}	0.4	1.0	0.1	0.6	1.0	kΩ
	OFF抵抗(5秒後) OFF-resistance (after 5 sec.)	R _{off}	3	3	2	3	1	MΩ
	常時最大許容損失 Max. cont. power dissipation	P _{CM}	70	70	70	70	70	mW
	常時最大印加電圧 Max. cont. voltage	V _{CM}	150	150	150	150	300	Vdc
	応答時間 Response time	Tr	1	2	1	2	1	ms
	減衰 Fall	T _f	40	60	60	80	40	ms
共通 Common	絶縁耐圧 Isolation voltage	V _{ISO}	800			3000		Vdc
	動作温度 Operating temperature range	T _{opr}			-20 ~ +60			°C
	保存温度 Storage temperature range	T _{stg}			-30 ~ +70			°C
	特長 Features		高品質 High reliability	低歪率 Low distortion	汎用型 General purpose	微少信号用 Low input current	低価格 Low cost	

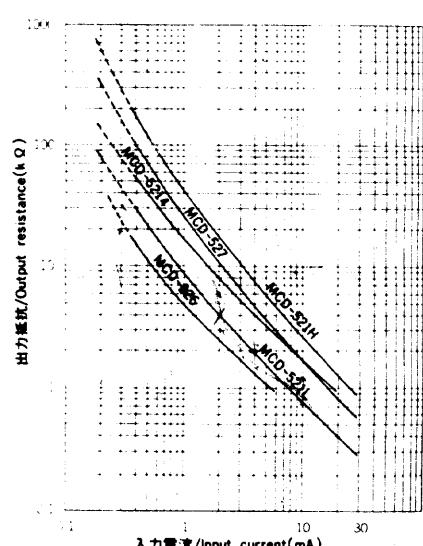
注記

- ① 出力側に赤色ペイントで表示があります。
- ② 出力側に黄色ペイントで表示があります。
- ③ 推奨時、負荷抵抗10kΩ時です。

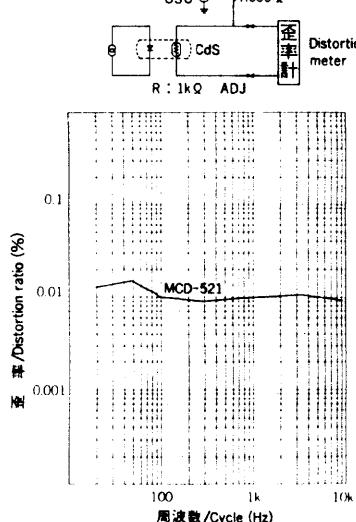
Notes

- ① Red mark is painted as a color code at output's side.
- ② Yellow mark is painted as a color code at output's side.
- ③ Measuring condition : R_L=10kΩ at each I_F.

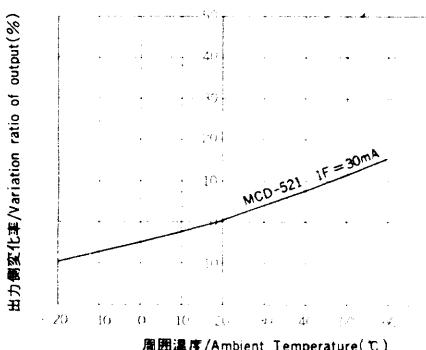
入力…出力特性
Output resistance vs.
Input current



歪率特性
Distortion ratio vs. Cycle



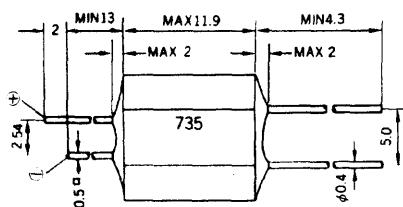
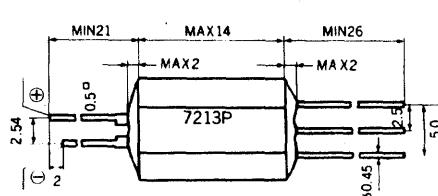
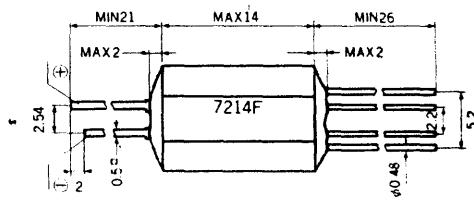
温度特性(代表例)
Temperature
Coefficient (Typ.)



MCD-7316・735・718

MCD-7213P

MCD-7214F

Boddy diameter $\phi 11.2$ Boddy diameter $\phi 10.0$ Boddy diameter $\phi 10.0$

unit: mm Ta=25°C

單位 Units	MCD-7316 MIN. TYP. MAX.	MCD-735 MIN. TYP. MAX.	MCD-718 MIN. TYP. MAX.	MCD-7213P MIN. TYP. MAX.	MCD-7214F MIN. TYP. MAX.	記号 Symbol	特 性 Characteristics	項目 Items
mW	100	100	100	100	60	P _{DM}	最大許容損失 Maximum power dissipation	入力側 (LED) Input
mA	50	35	40	50	30	I _{FM}	最大直流順電流 Maximum forward current	
mA	20	15	10	30	10	I _F	推奨順電流 Continuous forward current	
V	1.7	1.9	2.1	1.9	1.7	V _{FM}	順電圧(推奨時) Continuous forward voltage	
V	4	4	4	4	4	V _{RM}	最大直流逆電圧 Maximum reverse voltage	
kΩ	0.05	0.15	③8	26	④0.4 0.8	⑤4.0 7.0	R _{ON}	ON抵抗(推奨時) ON-resistance (at recommended voltage)
MΩ	1	1	2	1	1	R _{off}	OFF抵抗(5秒後) OFF-resistance (after 5 sec.)	
mW	70	70	72	60	40	P _{CM}	常時最大許容損失 Max. cont. power dissipation	
Vdc	100	100	350	150	150	V _{CM}	常時最大印加電圧 Max. cont. voltage	
ms	0.3	0.5	10	1	1	T _r	応答時間 Response time	
ms	300	200	50	50	50	T _f	減衰(37%) Fall	
Vdc	3000				V _{ISO}	絶縁耐圧 Isolation voltage		
°C	-20 ~ +60				T _{opr}	動作温度 Operating temperature range		
°C	-30 ~ +70				T _{stg}	保存温度 Storage temperature range		
	低抵抗 Low resistance	位相制御用 Phase control	二素子コモン付 Dual element	二素子独立 Two independent photocells	特長 Features			

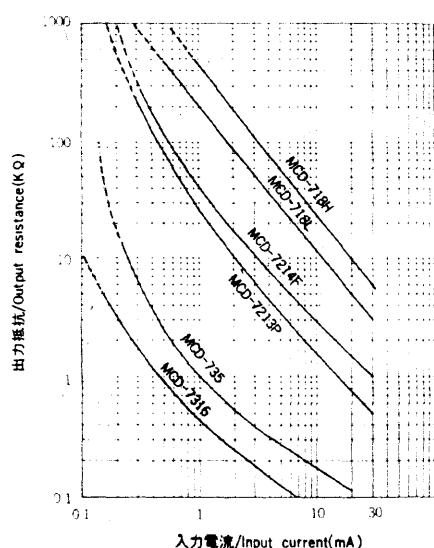
注記

- ① MCD-718は推奨電流に於けるランク分けがされています。
Hランク：赤色ペイント 12~26kΩ Lランク：黄色ペイント 8~16kΩ
- ② 推奨時二素子のバラツキは±25%以内です。
- ③ 推奨時二素子のバラツキは±30%以内です。

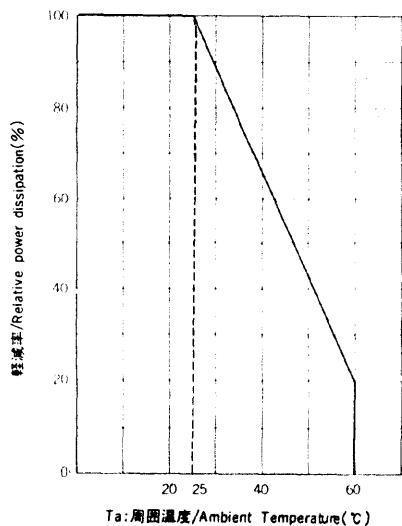
Notes

- ① ON resistance of MCD-718 is assorted into 2 ranks, H rank (Red dot 12~26kΩ) and L rank (Yellow dot 8~16kΩ) at I_F=10mA.
- ② Matching error is within ±25% at I_F=30mA.
- ③ Matching error is within ±30% at I_F=30mA.

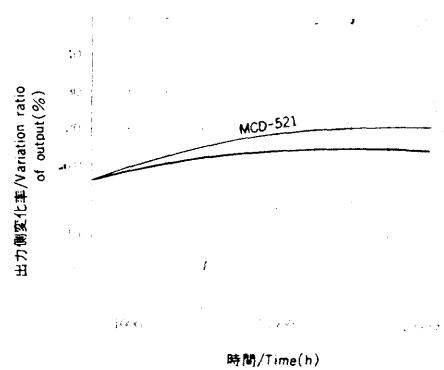
Fig2 入力一出力特性
Output resistance vs.
Input current



周囲温度一軽減率
Derating curve of power
dissipation



寿命特性
Life test



I_F = 30mA (25°C)
連続点灯/Constant mode

入力・出力特性 Output resistance vs. Input current

